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Digital electronics - Wikipedia

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Digital Systems Design Using VHDL, Jr., Charles H. Roth

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Hardware description language - Wikipedia

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VHDL es un lenguaje de especificación definido por el IEEE (Institute of Electrical and Electronics Engineers) (ANSI/IEEE 1076-1993) utilizado para describir circuitos digitales y para la automatización de diseño electrónico. VHDL es acrónimo proveniente de la combinación de dos acrónimos: VHSIC (Very High Speed Integrated Circuit) y HDL (Hardware Description Language).

VHDL - Wikipedia, la enciclopedia libre

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